

INITIAL INFORMATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 07-21-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Structures and Methods for
Selectively Applying A Well Bias to
Portions of a Programmable Device
Docket Number:: X-865-1D US
Request for Early Pub?:: No
Request for Non-Pub?:: No
Total Drawing Sheets:: 13
Small Entity?:: No
Petition included?:: No

INVENTOR INFORMATION

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: United Kingdom
Status:: Full Capacity
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State or Province:: CA
Postal or Zip Code:: 94306

Inventor Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Steven P.
Family Name:: Young
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City:: Boulder
State or Province:: CO

Postal or Zip Code:: 80303

 Inventor Authority Type:: Inventor
 Primary Citizenship Ctry:: US
 Status:: Full Capacity
 Given Name:: Stephen M.
 Family Name:: Trimberger
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 City:: San Jose
 State or Province:: CA
 Postal or Zip Code:: 95120

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	09/956,203	09-18-01

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
 Street:: 2100 Logic Drive
 City:: San Jose
 State or Province:: California
 Postal or Zip Code:: 95124